2025/11/12 23:15 1/1 x86 x64 SW

PCI PCIe CME2

1/2 SW 2025/11/12 23:15 x86 x64

x64 x86

SW

× PC OS

가

SW

가

PCI PCIe

OS

32 , 64

| | Case | | | |
|----|---------|----|----|--|
| 64 | OS - 64 | 64 | 64 | |
| 64 | OS - 32 | 64 | 32 | |
| 32 | OS - 32 | 32 | 32 | |
| 32 | OS - 64 | | 가 | |

===SW EtherCAT Master=== 1)

Daemon

OS

32 , 64

| | Case | Daemon | | |
|----|---------|--------|----|--|
| 64 | OS - 64 | 64 | 64 | |
| 64 | OS - 32 | 32 | 32 | |
| 32 | OS - 32 | 32 | 32 | |
| 32 | OS - 64 | | 가 | |

CME2

32 cme2 , 64

.cme2 32 64 cme2

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, 64 32 가 cEIP 32

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