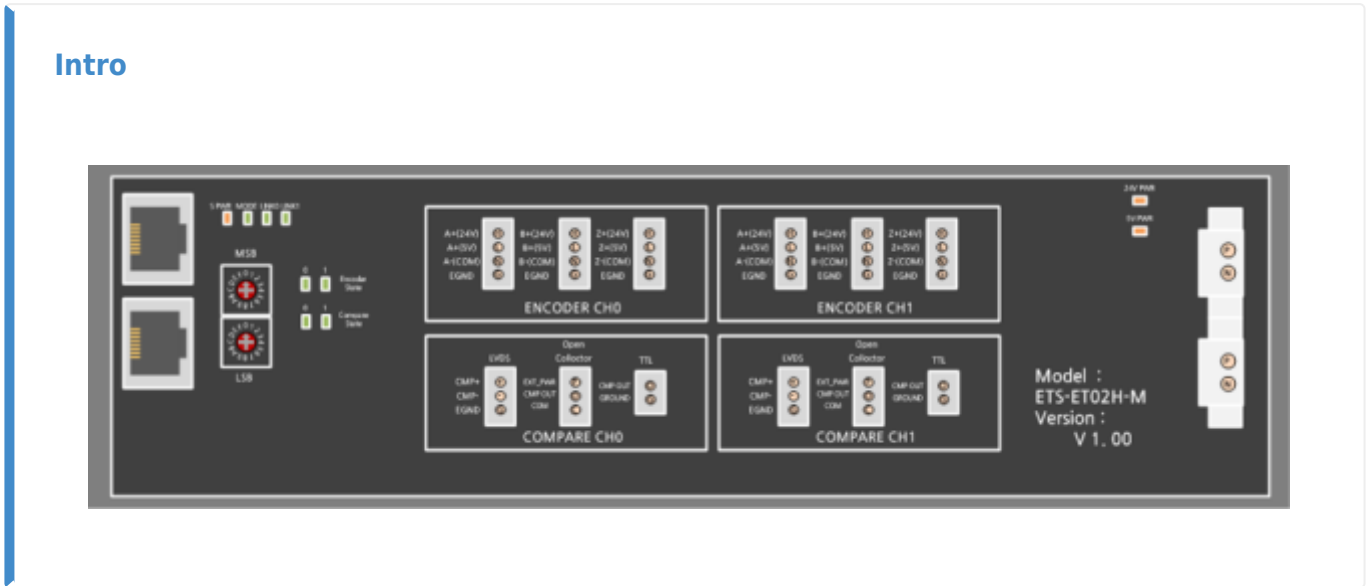


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# ETS-MT02H

× ETS-MT02H



- EtherCAT 2 Encoder Counter / Compare
- 2 Encoder , Encoder
- X, Y, X & Y Compare
- Margin, Margin
- 

### Function

#### Compare

- X, Y, X & Y : 2 Encoder Input &
- (FPGA) , 가

#### Margin

- X, Y X & Y Margin 가
- Margin , Trigger 가 Point
- Margin Vector

#### Feedback

- OutputData RAM
  - 2 (X, Y)
  - Depth : 2048 (32bit)
- Loss Compare position (Max 3 )

## Specification

- 2 CompareData Ram
  - size : 32bit
  - depth : 2048
- 2 OutputData Ram
  - size : 32bit
  - depth : 2048
- OutputType
  - Open Collector (24V, PNP) : 1Mbps
  - TTL, LVDS : 10Mbps

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